

Schematic:

PC BOARD CONNECTIONS

The schematic shows the electrical connections for the PC board. It includes 14 pins on the left (TD1+ to VC4-) and 8 pins on the right (J1 TX1+ to J8 TX4-). The connections are as follows: TD1+ to J1 TX1+, TD1- to J2 TX1-, TD2+ to J3 TX2+, TD2- to J6 TX2-, CT 4 to CT 5, TD3+ to J4 TX3+, TD3- to J5 TX3-, TD4+ to J7 TX4+, and TD4- to J8 TX4-. There are also four 1000pF/2KV capacitors connected to pins VC1 11, VC2 12, VC3 13, and VC4 14. A 4x 22nF/100V capacitor is connected to pins VC1 11, VC2 12, VC3 13, and VC4 14. A SHIELD is connected to pins VC1 11, VC2 12, VC3 13, and VC4 14.

The RJ45 connector schematic shows the internal wiring of the connector. It includes 8 pins (J1 TX1+ to J8 TX4-) and 4 pins (CT 4 to CT 5). The connections are as follows: J1 TX1+ to J2 TX1-, J2 TX1- to J3 TX2+, J3 TX2+ to J6 TX2-, J6 TX2- to J4 TX3+, J4 TX3+ to J5 TX3-, J5 TX3- to J7 TX4+, and J7 TX4+ to J8 TX4-. There are also four 1000pF/2KV capacitors connected to pins CT 4, CT 5, J4 TX3+, and J5 TX3-. A 4x 22nF/100V capacitor is connected to pins CT 4, CT 5, J4 TX3+, and J5 TX3-.

ELECTRICAL SPECIFICATIONS @25°C

1. Turn Ratio(±5%) :

Pri : Sec = 1CT : 1CT

2. Inductance OCL :

350uH MIN @100KHz/0. 1V, 8mA DC Bias

120uH MIN @100KHz/0. 1V, 18mA DC Bias

3. Insertion Loss :

-1. 0dB MAX @1-100MHz

-1. 2dB MAX @100-125MHz

4. Return Loss :

-16dB MIN @1-40MHz

-12dB MIN @40-60MHz

-10dB MIN @60-80MHz

-8dB MIN @80-100MHz

5. Cross talk :

-30dB MIN @1-100MHz

6. Common to Common Mode Rejection :

-30dB MIN @1-100MHz

7. DC Current/Voltage Rating pse Pins :

720mA MAX @57VDC (Continuous)

8. Hipot Test: 1500Vrms MIN

9. Operating Temperature: 0°C to 70°C.

REV.	ECN NO.	DESCRIPTION	DATE	APPD
A	REL		2018-02-26	

GREEN

LEFT LED

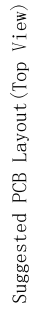
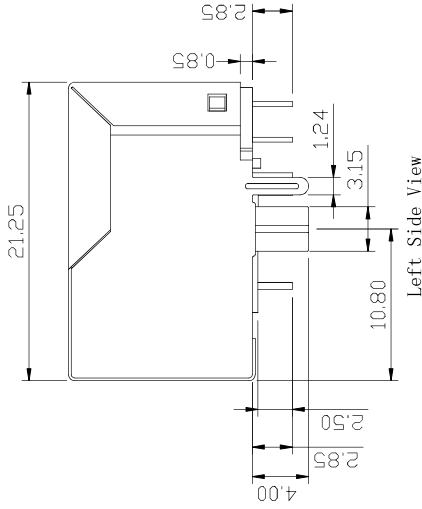
YELLOW

RIGHT LED

Summit Electronics BV			
TITLE: RJ45 Connector with 1000 Base-T Integrated Magnetics For PoE+ Application			
PART NO. : SRJ101010010		REV: A	

Mechanical:	REV.	ECN NO.	DESCRIPTION	DATE	APPD
				2018-02-26	
	A	REL			

REV.	ECN NO.	DESCRIPTION	DATE	APPD
A	REL		2018-02-26	



Technical drawing of the R1 connector showing a side view with dimensions: 3.80, 0.35, 0.50, 0.45, and a top view with dimension 0.50. The drawing is labeled 'R1' and 'cFUS'.

Bottom Side View

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X·X ±0.30		APPD: TOM	Summit Electronics BV	
X·XX ±0.25		CHKD: JAMES		
X·XXX ±0.05		DR: LEO		
ANGLES ±1°		UNIT: mm		
		SCALE: 2/1	SHEET: 2/2	REV: A
		PART NO. : SRJ101010010		
TITLE: RJ45 Connector with 1000 Base-T Integrated Magnetics For PoE+ Application				